Sites for example VHDL

<https://www.digikey.com/eewiki/display/LOGIC/Home>

<https://opencores.org/>

tt

Active HDL

<https://www.aldec.com/en/products/fpga_simulation/active-hdl>

Homework: :

1. Watch Active HDL tutorial videos
2. Study data sheet for DAC
3. Find/write SPI module (VHDL preferable)
4. Put in software request to IT to download Evaluation version of Active HDL
5. Simulate your SPI module with a test bench
6. Start Quartus project to write to DAC on BeMicro board using your SPI module that you tested